**ECE 593 Fundamentals of Pre-Silicon Validation**

**Project Code Drop 1 Report**

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* **Stimulus/Constraints-** We have added 3 constraints in out testbench.

1. Opcode
2. Immediate field value should not cause segmentation fault

* **Checking-**
* **Coverage-** We have added covergroups in our testbench.
* **Testbench structure-** We have created as of now

1. Package - typedef enum for opcodes
2. BFM – Interface with reset task
3. Transaction – Declared inputs for randomization and added constraints
4. Coverage – Added covergroups on internal signals of design
5. Generator – To provide constrained randomized set of instruction to the mailbox.

* **Simulation results-**